



Davison 4-7-23

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): K.L. Davison et al.
Case: 4-7-23
Serial No.: 10/722,652
Filing Date: November 26, 2003
Group: To Be Assigned
Examiner: To Be Assigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: V. Bencivenga Date: January 15, 2004

Title: Methods and Apparatus for Integrated Circuit Device
Power Distribution Via Internal Wire Bonds

TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop PGPUB Drawings
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attention: Official Draftsperson

Sir:

Applicants submit herewith three (3) sheets of formal drawings in the above-referenced application.

Respectfully submitted,

Date: January 15, 2004

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